

## General Description

SFGMOS<sup>®</sup> MOSFET is based on Oriental Semiconductor's unique device design to achieve low  $R_{DS(ON)}$ , low gate charge, fast switching and excellent avalanche characteristics. The high  $V_{th}$  series is specially optimized for high systems with gate driving voltage greater than 10V.

## Features

- Low  $R_{DS(ON)}$  & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Fast switching and soft recovery



## Applications

- Switched mode power supply
- Motor driver
- Battery protection
- DC-DC convertor
- Solar inverter
- UPS and energy inverter

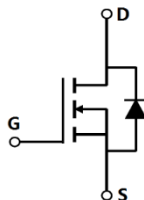
## Key Performance Parameters

Parameter	Value	Unit
$V_{DS, min} @ T_{j(max)}$	80	V
$I_{D, pulse}$	390	A
$R_{DS(ON), max} @ V_{GS}=10V$	3.3	m $\Omega$
$Q_g$	71.5	nC

## Marking Information

Product Name	Package	Marking
SFS08R03GNF	PDFN5*6	SFS08R03GN

## Package & Pin information



**Absolute Maximum Ratings** at  $T_j=25^{\circ}\text{C}$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	$V_{DS}$	80	V
Gate source voltage	$V_{GS}$	$\pm 20$	V
Continuous drain current <sup>1)</sup> , $T_C=25^{\circ}\text{C}$	$I_D$	130	A
Pulsed drain current <sup>2)</sup> , $T_C=25^{\circ}\text{C}$	$I_{D, pulse}$	390	A
Continuous diode forward current <sup>1)</sup> , $T_C=25^{\circ}\text{C}$	$I_S$	130	A
Diode pulsed current <sup>2)</sup> , $T_C=25^{\circ}\text{C}$	$I_{S, pulse}$	390	A
Power dissipation <sup>3)</sup> , $T_C=25^{\circ}\text{C}$	$P_D$	132	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	194	mJ
Operation and storage temperature	$T_{stg}, T_j$	-55 to 150	$^{\circ}\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	0.95	$^{\circ}\text{C/W}$
Thermal resistance, junction-ambient <sup>4)</sup>	$R_{\theta JA}$	62	$^{\circ}\text{C/W}$

**Electrical Characteristics** at  $T_j=25^{\circ}\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	80			V	$V_{GS}=0\text{ V}, I_D=250\ \mu\text{A}$
Gate threshold voltage	$V_{GS(th)}$	2.0		4.0	V	$V_{DS}=V_{GS}, I_D=250\ \mu\text{A}$
Drain-source on-state resistance	$R_{DS(ON)}$		3.0	3.3	m $\Omega$	$V_{GS}=10\text{ V}, I_D=50\text{ A}$
Gate-source leakage current	$I_{GSS}$			100	nA	$V_{GS}=20\text{ V}$
				-100		$V_{GS}=-20\text{ V}$
Drain-source leakage current	$I_{DSS}$			1	$\mu\text{A}$	$V_{DS}=80\text{ V}, V_{GS}=0\text{ V}$
Gate resistance	$R_G$		2.6		$\Omega$	$f=1\text{ MHz}, \text{Open drain}$

### Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	$C_{iss}$		5661		pF	$V_{GS}=0\text{ V}$ , $V_{DS}=25\text{ V}$ , $f=100\text{ kHz}$
Output capacitance	$C_{oss}$		1631		pF	
Reverse transfer capacitance	$C_{rss}$		95.2		pF	
Turn-on delay time	$t_{d(on)}$		28		ns	$V_{GS}=10\text{ V}$ , $V_{DS}=40\text{ V}$ , $R_G=2\ \Omega$ , $I_D=50\text{ A}$
Rise time	$t_r$		10		ns	
Turn-off delay time	$t_{d(off)}$		60.2		ns	
Fall time	$t_f$		14		ns	

### Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	$Q_g$		71.5		nC	$V_{GS}=10\text{ V}$ , $V_{DS}=40\text{ V}$ , $I_D=50\text{ A}$
Gate-source charge	$Q_{gs}$		21		nC	
Gate-drain charge	$Q_{gd}$		10		nC	
Gate plateau voltage	$V_{plateau}$		4.4		V	

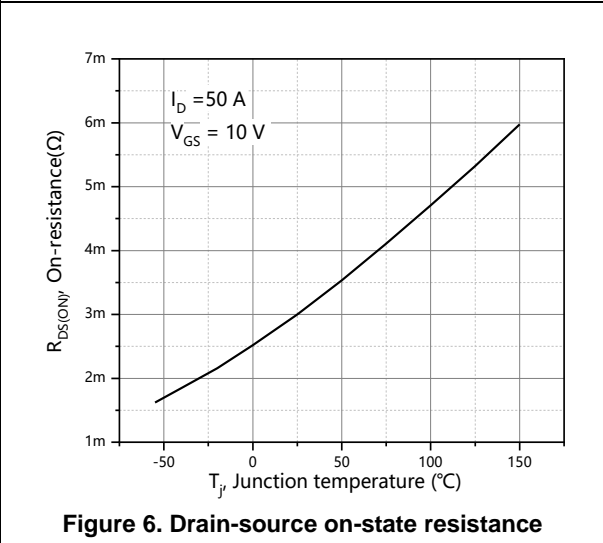
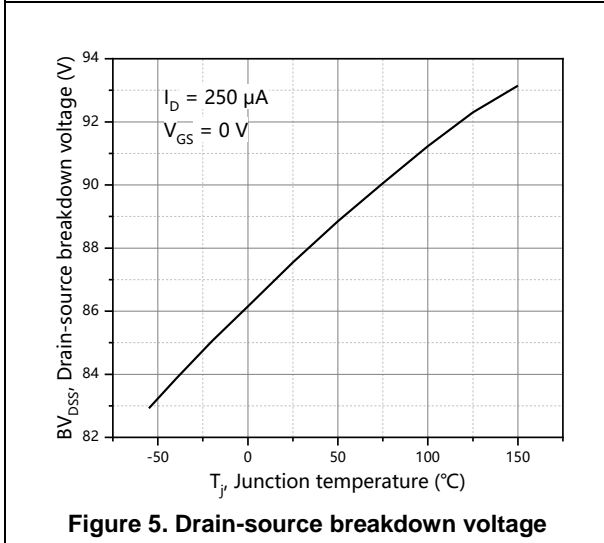
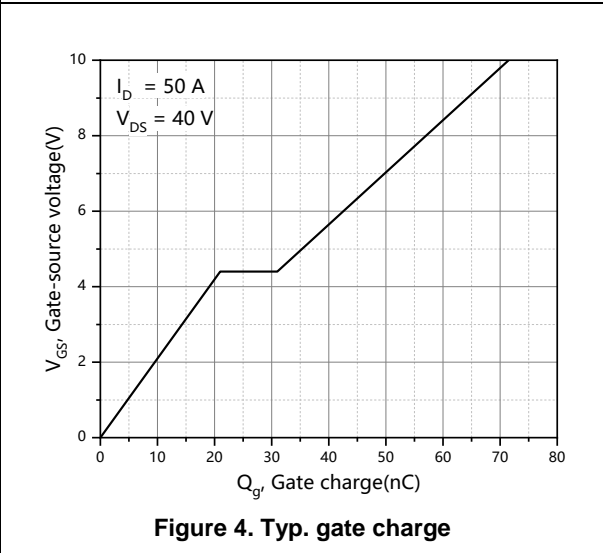
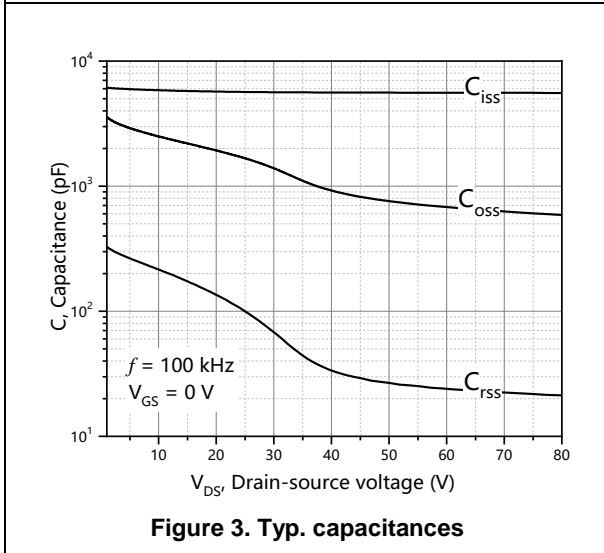
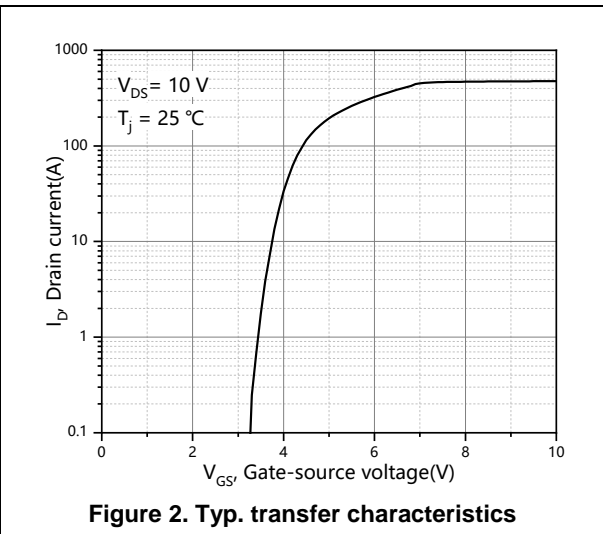
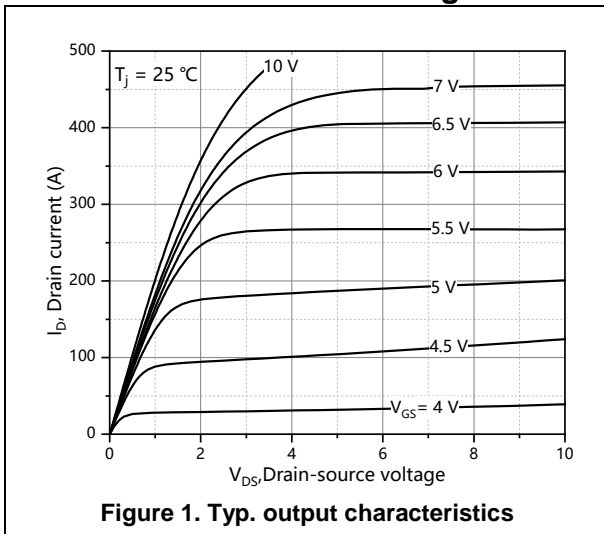
### Body Diode Characteristics

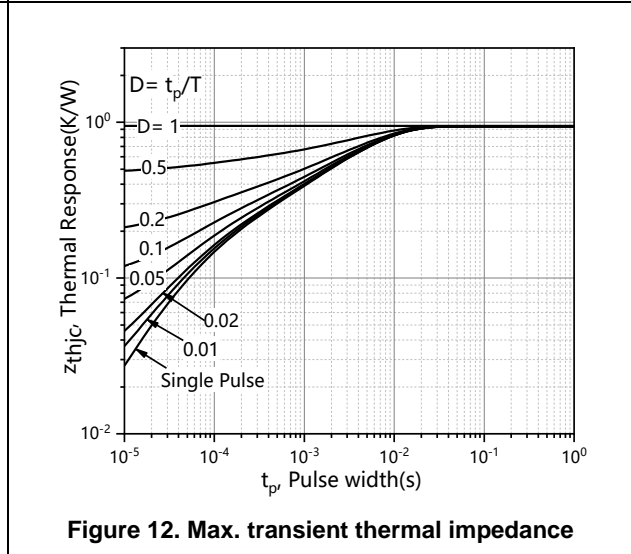
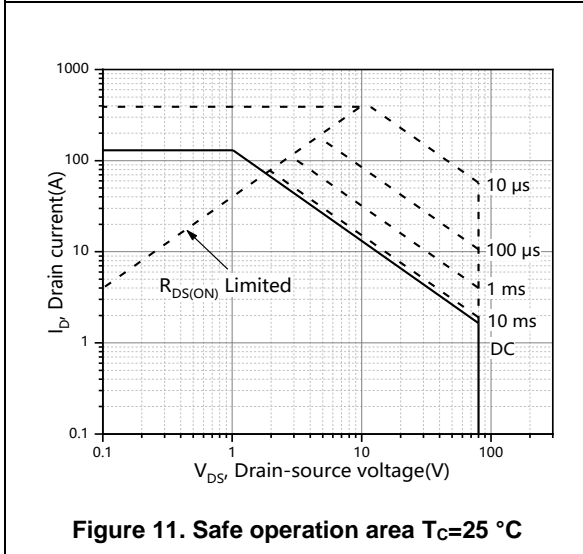
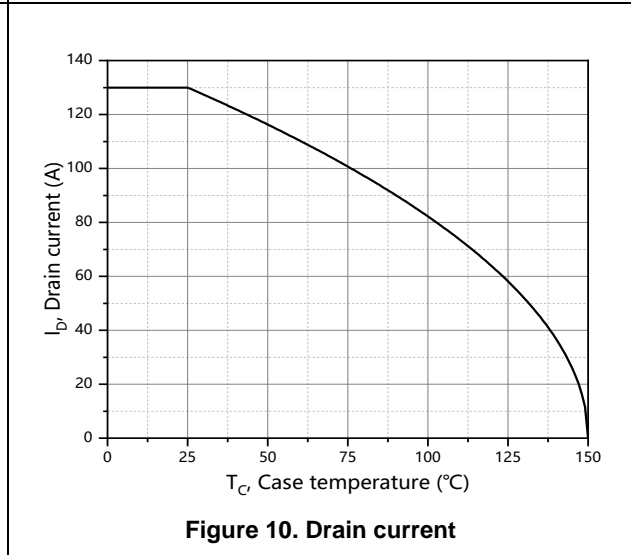
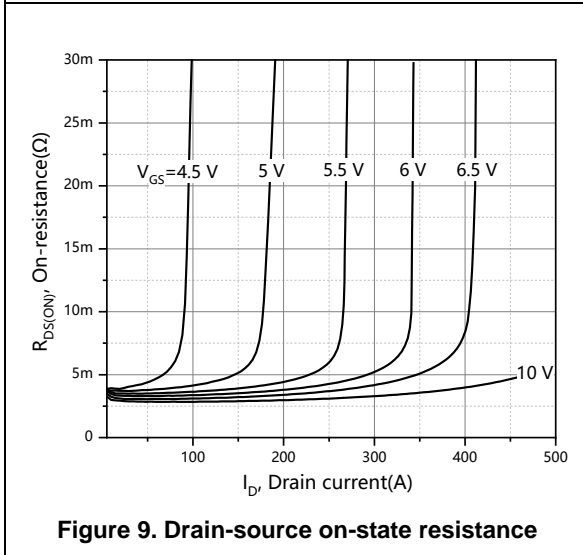
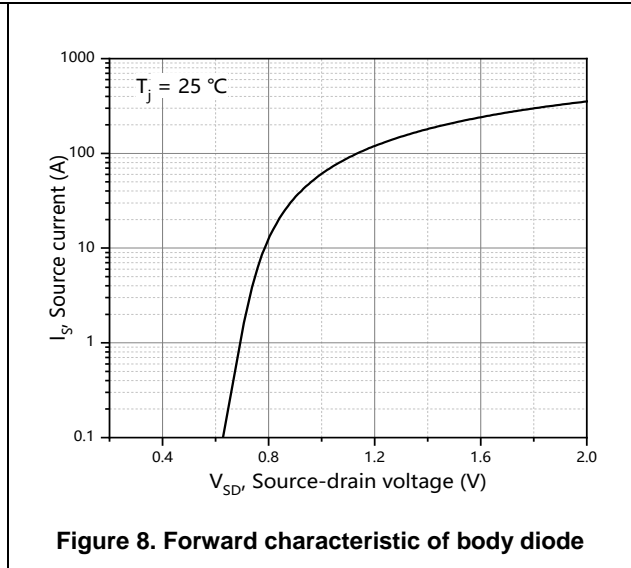
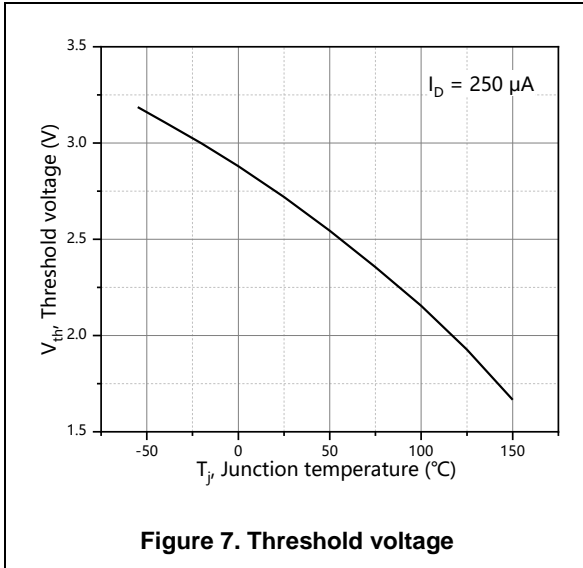
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	$V_{SD}$			1.3	V	$I_S=12\text{ A}$ , $V_{GS}=0\text{ V}$
Reverse recovery time	$t_{rr}$		67.2		ns	$V_R=50\text{ V}$ , $I_S=50\text{ A}$ , $di/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge	$Q_{rr}$		87.9		nC	
Peak reverse recovery current	$I_{rrm}$		2.2		A	

### Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3)  $P_d$  is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_a=25\text{ }^\circ\text{C}$ .
- 5)  $V_{DD}=50\text{ V}$ ,  $V_{GS}=10\text{ V}$ ,  $L=0.3\text{ mH}$ , starting  $T_j=25\text{ }^\circ\text{C}$ .

**Electrical Characteristics Diagrams**





**Test circuits and waveforms**



**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

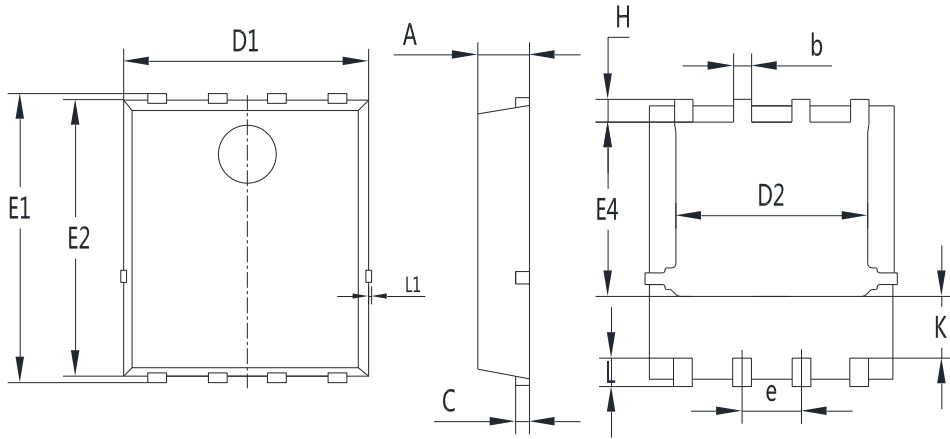


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

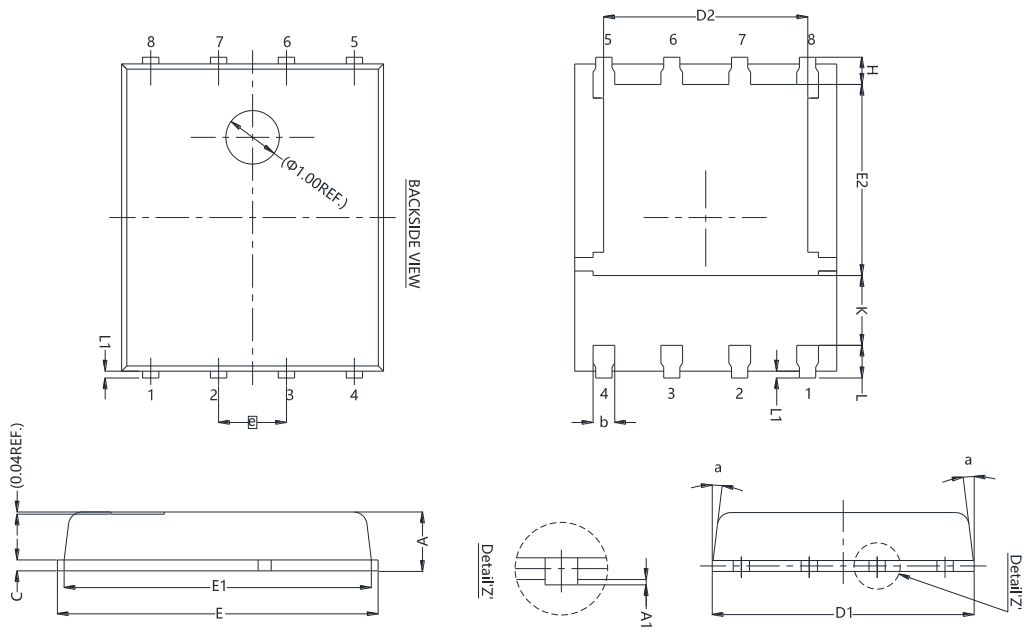
**Package Information**



Symbol	mm		
	Min	Nom	Max
A	1.00	1.10	1.20
b	0.30	0.40	0.50
c	0.154	0.254	0.354
D1	5.00	5.20	5.40
D2	3.80	4.10	4.25
e	1.17	1.27	1.37
E1	5.95	6.15	6.35
E2	5.66	5.86	6.06
E4	3.52	3.72	3.92
H	0.40	0.50	0.60
L	0.30	0.60	0.70
L1	0.12 REF		
K	1.15	1.30	1.45

Version 1: PDFN5\*6-C package outline dimension

**Package Information**



Symbol	mm		
	Min	Nom	Max
A	0.90	1.00	1.10
A1	0.00	-	0.05
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
e	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
a	0°	-	12°

Version 2: PDFN5\*6-M package outline dimension



### Ordering Information

Package Type	Units/ Reel	Reels / Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
PDFN5*6-C	5000	2	10000	5	50000
PDFN5*6-M	5000	2	10000	5	50000

### Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFS08R03GNF	PDFN5*6	yes	yes	yes

### Legal Disclaimer

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